SPI Guide for W3150A+ & W5100

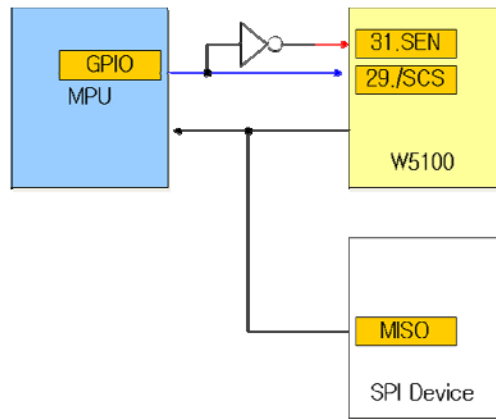**(* including all NM modules and Evaluation Board using W3150A+ or W5100)**

1. Cause and Symptom

   When using SPI mode of W3150A+ or W5100, MISO output is driven even though /SCS is asserted high. When SPI master (normally MCU) reads or writes another SPI device on the SPI BUS, it can't be normally processed. If there is not any other device on the SPI BUS, there is not any problem in read and write.
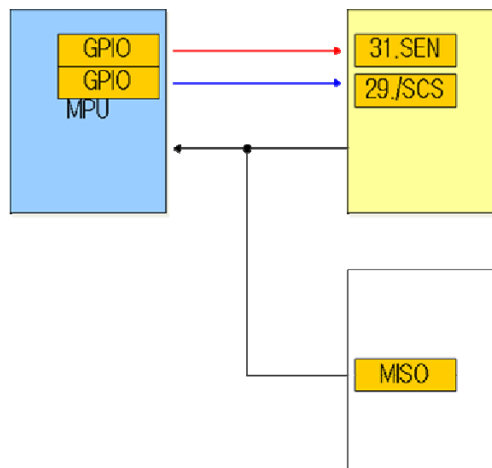
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2. Solution

   When access to another device on the SPI BUS, assert the SPI_EN pin of W3150A+ or W5100 as low first and access other devices. When access W3150A+ or W5100 again, the SPI_EN pin should be high.

   EX 1) By using inverter

   EX2) By using I/O port of MCU

3. Guide for related products

Chip	W3150A+, W5100	When using another device on the SPI BUS, above guide should be applied.
NM Module	NM7010B+, WIZ810MJ	
ASRB Board	ASRB-Serial, ASRB-Parallel, ASRB-USB	When implementing SPI by referring ASRB or EVB, user should follow above guide.
EVB	EVB-B1+, W5100E01-AVR	