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# propellerforth

*Interactive ANS-subset  
Forth for the Parallax  
Propeller microcontroller*

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## PropellerTricks

*Some useful tricks when writing  
Propeller assembly language*

Updated Dec 19, 2007 by [cbiffle](#)

# Aligning Shared Pointers

Shared RAM is byte-addressed, but when addressing multibyte quantities the low-order bits of the address are ignored. In other words, 0, 1, 2, and 3 all address the same 32-bit quantity.

This has a useful property. When building a data structure (like [PropellerForth's](#) dictionary), it's common that cell- or halfcell-sized fields need to be aligned on a cell or halfcell boundary -- but if the position isn't aligned, writing a multibyte value will overwrite the previously written bytes. However, adding 1 (for halfcells) or 3 (for cells) will effectively align the pointer to the next available location.

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